

Wainganga Bahu-Uddeshiya Vikas Sanstha's

WAINGANGA COLLEGE OF ENGINEERING AND MANAGEMENT

An Autonomous Institute

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www.wcem.edu.in

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WCEM, EXAMINATION CELL

Date: 07/11/2025

End Semester Examination (ESE) Winter-2025 Timetable for Master of Technology (MTech) First Semester Programme: Written

Time: 11.00 AM To 02.00 PM

Sr.	Name of Branch	Date	03-01-26	06-01-26	08-01-26	10-01-26	13-01-26	15-01-26
No		Day	Saturday	Tuesday	Thursday	Saturday	Tuesday	Thursday
			PGAIDS101T	PGAIDS102T	PGAIDS104T	PGAIDS106T	PGAIDS107T	PGAIDS108T
1	Artificial Intelligence and Data Science (AIDS)		Artificial Intelligence and Expert Systems	Machine Learning	Advanced Algorithm	Research Methodology and Intellectual Property Rights	Program Elective-I 1. Big Data Analytics 2. Blockchain Application Development	Program Elective-II 1. Sensor Network & Internet of things 2. Digital Forensics
2	Computer Aided Structural Engineering (CASE)		PGCASE101T	PGCASE103T	PGCASE104T	PGCASE106T	PGCASE107T	PGCASE108T
			Advanced Concrete Structure Time: *11.00 AM To 03.00 PM	Structural Dynamics	Foundation Design	Theory of Elasticity	Program Elective-I 1. Matrix Analysis of Structures 2. Earthquake Resistant Design of Steel Structures	Program Elective-II 1. Road Safety Engineering 2. Global Warming and Climate Change

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No		Saturday	Tuesday	Thursday	Saturday	Tuesday	Thursday
3	Computer Science and Engineering (CSE)	PGCSE101T	PGCSE103T	PGCSE105T	PGCSE106T	PGCSE107T	PGCSE108T
		Advanced Computing Architecture	Advances in Operating System Design	Data Science using Python	Digital Image Processing & Computer Vision	Elective-I 1. Pattern Recognition 2. Optimization Techniques in Artificial Intelligence	1. Advance Data Mining and Big Data Analytics 2. Cyber Forensic and Computer Crimes
4	CAD/CAM	PGCADCAM101T	PGCADCAM 102T	PGCADCAM 103T	PGCADCAM104T	PGCADCAM105T	PGCADCAM106T
		Computer Integrated Manufacturing	Computer Graphics	CNC and Robotics	Elective-I 1. Design for Manufacturing & Assembly 2. Material Engineering Mechanical behaviour of Engineering Materials	Elective-II 1. Total Quality System & Engineering 2. Concurrent Engineering Advanced Machining Process	Foundation Course- 1

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No		Saturday	Tuesday	Thursday	Saturday	Tuesday	Thursday
5	Electronics Engineering (EE)	PGETRX101T	PGETRX103T	PGETRX104T	PGETRX106T	PGETRX107T	PGETRX108T-2
		Advanced Digital	CMOS VLSI	Advanced	Program Elective-I	Research	Open Elective I
		Signal Processing	Design	Embedded System	1. Pattern Recognition	Methodology	1. Advance Data Mining
		F18 140 S F1 F1		Design	2. Analog IC Design		and Big Data Analytics
			Fr 200 to 700		3. Advanced Digital	E - CEST TO LET	2. Optimization
					Communication		Techniques in Artificial
							Intelligence
							3. Cyber Forensic and
							Computer Crimes
6	Power Electronics and Power System (PEPS)	PGPEPS101T	PGPEPS103T	PGPEPS104T	PGPEPS106T	PGPEPS107T	PGPEPS108T
						Program Elective-I	Group Elective
		Advanced Power Electronics Advanced Power System Protection	Advanced	FACTS & HVDC Transmission	Research Methodology	1. Microcontroller	1. Electrical Power
			Power System			Application in	Distribution & Smart
						PE & PS	Grid
					2. Hybrid Electric	2. AI Application to	

Copy to:

- 1. Director
- 2. All MTech HoD's
- 3. Examination Centre
- 4. Admin Office

Issued by: Dr. Sachin Y. Zade

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